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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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PATENT  
MAR-3 2002  
TO MAIL ROOM.

#8  
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In re Application: Dong Suk SHIN

Serial No.: 10/034,243

Filed: December 28, 2001

For: METHOD OF FABRICATING A  
SEMICONDUCTOR DEVICE HAVING  
REDUCED CONTACT RESISTANCE

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THE COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Applicants submit herewith three Japanese patent applications of which the applicants are aware, and which may be material to the examination of this application and in respect of which there may be a duty to disclose under 37 CFR 1.56.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 CFR 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.



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TO TELEMAIL ROOM

The references submitted herewith are listed on PTO-Form 1449 (modified) attached hereto. A copy of each reference listed patent is being furnished.

Additional comments, if any, on the relevance of each reference listed are provided as follows:

Full translations of the Japanese applications are not readily available, but a Japanese language Abstract is attached to each of the Japanese applications

The Statement is made on the basis of the information:

       supplied by the inventor(s);  
  X   supplied by an individual associated with the filing and prosecution  
       of this application (37 CFR 1.56(c)); or  
       in the attorney's file.

Respectfully submitted,

Feb. 19, 2002  
Date

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(Form PTO-1449)